

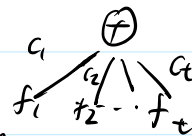
We say a polynomial is a quadratic form if it is homogeneous of degree 2, and a linear form if it is homogeneous of degree 1.

Def: The multiplicative complexity of a set $\{f_1, \dots, f_m\}$ of quadratic forms in X_1, \dots, X_n is

$$L(f_1, \dots, f_m) := \min_{\substack{C \text{ computing} \\ f_1, \dots, f_m \\ \text{simultaneously}}} (\# \text{ of multiplication gates of } C)$$

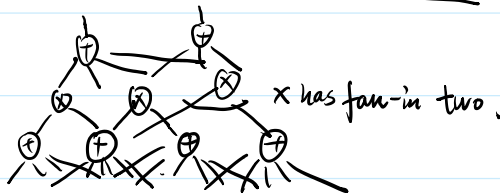
Assume the addition gates

can compute $\sum c_i f_i$ now



Def: A quadratic circuit is a ^{multi-output} circuit of unbounded fan-in that has the form $\sum (\sum \times \sum)$.

So it looks like:



with minimum \times gates

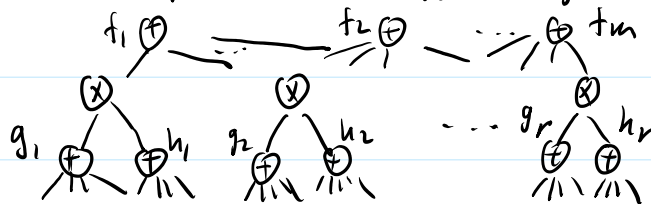
We would like to show that to compute (one or several) quadratic forms, it suffices to use quadratic circuits.

(Strassen)

Thm 1: For quadratic forms f_1, \dots, f_m ,

$$L(f_1, \dots, f_m) = \min \left\{ r : \exists \left. \begin{array}{l} \text{linear forms } g_1, \dots, g_r, h_1, \dots, h_r \\ \text{such that } f_1, \dots, f_m \in \text{span}\{g_i h_i, \dots, g_r h_r\} \end{array} \right\} \right.$$

Pf: \leq : Suppose $f_1, \dots, f_m \in \text{span}\{g_i h_i, \dots, g_r h_r\}$. Then just build the quadratic circuit

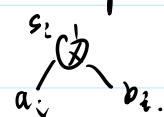


multiplication gates = r.

\geq : Let C be a ^(not necessarily quadratic) circuit computing f_1, \dots, f_m with $r = L(f_1, \dots, f_m)$ multiplication gates.

Let s_1, \dots, s_r be the outputs of the r multiplication gates s.t. s_i depends only on s_1, \dots, s_{i-1} . (via a topological sort)

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Suppose $s_i = a_i \cdot b_i$. 

Recall $\text{Hom}_k(f)$ denotes the homogeneous degree- k part of f .

Claim : $\text{Hom}_2(s_i) \in \text{span}\{\text{Hom}_1(a_1) \cdot \text{Hom}_1(b_1), \dots, \text{Hom}_1(a_i) \cdot \text{Hom}_1(b_i)\}$ for $i=1, \dots, r$.

This claim is proved by induction on i .

$$\text{Note } \text{Hom}_2(s_i) = \text{Hom}_2(a_i \cdot b_i) = \text{Hom}_0(a_i) \cdot \text{Hom}_2(b_i) + \text{Hom}_1(a_i) \cdot \text{Hom}_1(b_i) + \text{Hom}_2(a_i) \cdot \text{Hom}_0(b_i). \quad (*)$$

Base case: $i=1$. As the computation of a_1 and b_1 does not use multiplication, $\deg(a_1), \deg(b_1) \leq 1$. So $\text{Hom}_2(a_1) = \text{Hom}_2(b_1) = 0$.

$$\Rightarrow \text{Hom}_2(s_1) = \text{Hom}_1(a_1) \cdot \text{Hom}_1(b_1).$$

Now consider $i > 1$. By (*) and the fact that $\text{Hom}_0(a_i), \text{Hom}_0(b_i) \in \mathbb{F}$,

we just need to show $\text{Hom}_2(a_i), \text{Hom}_2(b_i) \in \text{span}\{\text{Hom}_1(a_1) \cdot \text{Hom}_1(b_1), \dots, \text{Hom}_1(a_i) \cdot \text{Hom}_1(b_i)\}$.

Note a_i and b_i are linear combinations over \mathbb{F} of elements in

$$\mathbb{F} \cup \{x_1, \dots, x_n\} \cup \{s_1, \dots, s_{i-1}\}.$$

$$\text{So } \text{Hom}_2(a_i), \text{Hom}_2(b_i) \in \text{span}\{s_1, \dots, s_{i-1}\}$$

$$\stackrel{\text{induction hypothesis}}{\rightarrow} \subseteq \text{span}\{\text{Hom}_1(a_1) \cdot \text{Hom}_1(b_1), \dots, \text{Hom}_1(a_i) \cdot \text{Hom}_1(b_i)\}$$

This proves the claim.

For $i=1, \dots, m$, the output f_i of C is a linear combination over \mathbb{F} of elements in

$$\mathbb{F} \cup \{x_1, \dots, x_n\} \cup \{s_1, \dots, s_r\}.$$

As f_i is homogeneous of degree 2, $f_i = \text{Hom}_2(f_i) \in \text{span}\{\text{Hom}_2(s_1), \dots, \text{Hom}_2(s_r)\}$

$$\stackrel{\text{by the claim}}{\rightarrow} \subseteq \text{span}\{\text{Hom}_1(a_1) \cdot \text{Hom}_1(b_1), \dots, \text{Hom}_1(a_r) \cdot \text{Hom}_1(b_r)\}$$

So $r = \mathcal{L}(f_1, \dots, f_m)$ is \leq RHS of Thm 1. □

So $r = L(f_1, \dots, f_m)$ is \leq RHS of Thm 1. □

A reformulation of Thm 1 is that there always exists a quadratic circuit computing f_1, \dots, f_m with $L(f_1, \dots, f_m)$ multiplications.

We say $f \in \mathbb{F}[X_1, \dots, X_n, Y_1, \dots, Y_m]$ is a bilinear form in (X_i) and (Y_i) if every monomial has the form $X_i Y_j$.

We say a quadratic circuit is a bilinear circuit in (X_i) and (Y_i) if every multiplication gate has the form $g \cdot h$
 bilinear forms in (X_i) and (Y_i) ↳ g is a linear form in X 's, h in Y 's.

Thm 2: Suppose f_1, \dots, f_p are computed by a quadratic circuit with r multiplication gates.

Then they are computed by a bilinear circuit with $2r$ multiplication gates.

Pf: We know $f_1, \dots, f_p \in \text{span} \{g_i h_i, \dots, g_r h_r\}$ for linear forms g_1, \dots, g_r in $\{X_i\} \cup \{Y_i\}$

Each $(g_i h_i)(X, Y) = g_i(X, 0) h_i(X, 0) + g_i(X, 0) h_i(0, Y) + g_i(0, Y) h_i(X, 0) + g_i(0, Y) h_i(0, Y)$ by linearity.

As f_1, \dots, f_p are bilinear in (X_i) and (Y_i) ,

$f_1, \dots, f_p \in \text{span} \{g_i(X, 0) h_i(0, Y), g_i(0, Y) h_i(X, 0) : 1 \leq i \leq r\}$. □

Denote by $L^*(f_1, \dots, f_p)$ the # multiplication gates needed in a bilinear circuit computing f_1, \dots, f_p .

By Thm 2, $L(f_1, \dots, f_p) \leq L^*(f_1, \dots, f_p) \leq 2L(f_1, \dots, f_p)$.

Tensor rank.

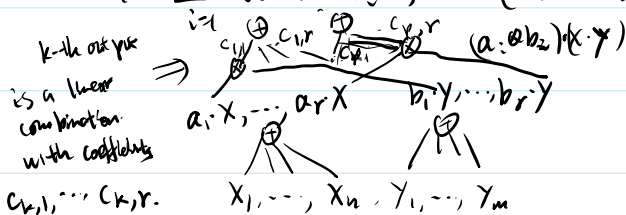
Def: The tensor rank of a tensor $T \in \mathbb{F}^n \times \mathbb{F}^m \times \mathbb{F}^p$ is $\min_r \left\{ T = \sum_{i=1}^r a_i \otimes b_i \otimes c_i, \begin{matrix} a_1, \dots, a_r \in \mathbb{F}^n \\ b_1, \dots, b_r \in \mathbb{F}^m \\ c_1, \dots, c_r \in \mathbb{F}^p \end{matrix} \right\}$ pure tensor.

Thm 3: Let f_1, \dots, f_p be bilinear forms in X_1, \dots, X_n and Y_1, \dots, Y_m with $f_k = \sum_{\substack{1 \leq i \leq n \\ 1 \leq j \leq m}} c_{ij} X_i Y_j$

Let $T \in \mathbb{F}^n \otimes \mathbb{F}^m \otimes \mathbb{F}^p$ be a tensor defined by $T(i, j, k) = c_{ij} k$.

Then $L^*(f_1, \dots, f_p) = \text{rank}(T)$.

Pf: \geq : Let $T = \sum_{i=1}^r a_i \otimes b_i \otimes c_i$, $a_i = (a_{i,1}, \dots, a_{i,n})$, $b_i = (b_{i,1}, \dots, b_{i,m})$, $c_i = (c_{i,1}, \dots, c_{i,p})$.



build a bilinear circuit with r multiplication gates.

\leq : reverse the usual decomposition $T = \sum_{i=1}^r a_i \otimes b_i \otimes c_i$

$$c_1, \dots, c_k, r, \quad x_1, \dots, x_n, \quad y_1, \dots, y_m$$

\Leftarrow : reverse the proof to get a decomposition $T = \sum_{i=1}^r a_i \otimes b_i \otimes c_i$ from the circuit. \square

Matrix Multiplication.

Consider the problem of matrix multiplication: compute $(Z_{ik})_{\substack{1 \leq i \leq n \\ 1 \leq k \leq p}} = (X_{ij})_{\substack{1 \leq i \leq n \\ 1 \leq j \leq m}} \cdot (Y_{jk})_{\substack{1 \leq j \leq m \\ 1 \leq k \leq p}}$

$$Z_{ik} = \sum_{j=1}^m X_{ij} \cdot Y_{jk} \text{ is bilinear in } (X_{ij}) \text{ and } (Y_{jk}), \quad 1 \leq i \leq n, \quad 1 \leq k \leq p$$

The corresponding tensor is $\langle n, m, p \rangle := \sum_{i=1}^n \sum_{k=1}^p \left(\sum_{j=1}^m e_{ij}^x e_{jk}^y \right) e_{ik}^z \in \mathbb{F}^{nm} \otimes \mathbb{F}^{mp} \otimes \mathbb{F}^{np}$ where the (i,j) -th entry of e_{ij}^x is 1 and the others are 0.

Define $\omega := \inf (\log_n \text{rank}(\langle n, n, n \rangle))$, called the matrix multiplication exponent.

Note $n^2 \leq \text{rank}(\langle n, n, n \rangle) \leq n^3$. So $2 \leq \omega \leq 3$.

Thm For any constant ϵ , there is an $O(n^{\omega+\epsilon})$ -time algorithm computing the product of (X_{ij}) and (Y_{jk}) (assuming n and x take unit time.)

Pf: By definition, $\exists n_0$ depending only on ϵ s.t. $\text{rank}(\langle n_0, n_0, n_0 \rangle) \leq n_0^{\omega+\epsilon}$.

View (X_{ij}) and (Y_{jk}) as $n_0 \times n_0$ block matrices with block size $(n/n_0) \times (n/n_0)$

By Thm 3, \exists algorithm A_0 computing $n_0 \times n_0$ matrix multiplication with $\leq n_0^{\omega+\epsilon}$ multiplications and $O(n)$ additions.

Recursively multiply (X_{ij}) and (Y_{jk}) : using A_0 , with entries replaced by $(n/n_0) \times (n/n_0)$ blocks.

$$T(n) \leq n_0^{\omega+\epsilon} \cdot T(n/n_0) + O(n^2) \Rightarrow T(n) \leq n^{\omega+\epsilon}$$

Multiplication of blocks takes the $T(n/n_0)$

Addition takes time $O((n/n_0)^2) = O(n^2)$

\square

Similarly, the time complexity of multiplying $n \times m$ and $m \times p$ matrices can be bounded in terms of $\inf_k (\log \text{rank}(\langle nk, mk, pk \rangle))$.

Lemma: $\text{rank}(\langle n, m, p \rangle) = \text{rank}(\langle \sigma(n), \sigma(m), \sigma(p) \rangle)$ for any permutation σ of $\{n, m, p\}$.

Pf. Recall $\langle n, m, p \rangle = \sum_{\substack{1 \leq i \leq n \\ 1 \leq j \leq m \\ 1 \leq k \leq p}} e_{ij}^x e_{jk}^y e_{ik}^z$. We may rename e_{ik}^z by $e_{ki}^z \Rightarrow \langle n, m, p \rangle = \sum_{\substack{1 \leq i \leq n \\ 1 \leq j \leq m \\ 1 \leq k \leq p}} e_{ij}^x e_{jk}^y e_{ki}^z$.

Then cyclically permutating x, y, z sends $\langle n, m, p \rangle$ to $\langle \sigma(n), \sigma(m), \sigma(p) \rangle$ with cyclic permutation of n, m, p .

The 3 transpositions are handled by renaming $e_{ij}^* \rightarrow e_{ji}^*$, $*$ = x, y, z , together with cyclic permutation. \square

So, e.g., multiplying $n \times m$ and $m \times p$ matrices, and multiplying $p \times n$ and $n \times m$ matrices leads to $O(n^{\omega+\epsilon})$.

So, e.g., multiplying $n \times m$ and $m \times p$ matrices, and multiplying $p \times n$ and $n \times m$ matrices have the same complexity, together with cyclic permutations. \square

Thm: If $\text{rank}(n, m, p) \leq r$, then $\omega \leq \log_{(nmp)^{1/3}} r = 3 \log_{nmp} r$.

Pf: Note $\text{rank}(T \otimes T') \leq \text{rank}(T) \cdot \text{rank}(T')$.

And $\langle n, n', m, m', p, p' \rangle = \langle n, m, p \rangle \otimes \langle n', m', p' \rangle$.

So $\langle nmp, nmp, nmp \rangle = \langle n, m, p \rangle \otimes \langle m, p, n \rangle \otimes \langle p, n, m \rangle$.

Then $\text{rank}(\langle nmp, nmp, nmp \rangle) = (\text{rank}(\langle n, m, p \rangle))^3 \leq r^3$.

$\Rightarrow \omega \leq \log_{nmp} r^3 = 3 \log_{nmp} r$. \square

Thm (Strassen '69) $\text{rank}(\langle 2, 2, 2 \rangle) \leq 7 \Rightarrow \omega \leq \log_2 7 = 2.807\dots$

We now know $\text{rank}(\langle 2, 2, 2 \rangle) = 7$ (over $\mathbb{F} = \mathbb{C}$). (Wingard '71).

Computing the tensor rank is NP-hard (Håstad '90).

Current record of ω (William-Xu-Xu-Zhou '23): $\omega \leq 2.371552$.

Conjecture: $\omega = 2$.

Note: If the conjecture is false, then $\text{rank}(\langle n, n, n \rangle) = \Omega(n^{2+\epsilon})$ for some $\epsilon > 0$.

Then by Baur-Strassen, $\sum_{1 \leq i, j, k \leq n} X_{ij} \cdot Y_{jk} \cdot Z_{ik}$ has general circuit lower bound $\Omega(n^{2+\epsilon})$

So a disproof of the conjecture improves the best known general circuit lower bound!

$= \Omega(N^{1+\epsilon/2})$, where $N = 3n^2 = \# \text{ variables}$.

Related work: (Andrews '22): Either $\omega = 2$, or there is a nontrivial PIT algorithm for circuits where $\#$ multiplication gates is used as the complexity measure.

(Raz '13) Explicit tensor $T: [n]^r \rightarrow \mathbb{F}$ of tensor rank $\geq n^{r(1-o(1))}$, where

$\omega(n) \leq r \leq \log n / \log \log n$, gives a superpolynomial lower bound for the size of general algebraic circuits.